

EV317135835

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. .... 09/932,040  
Priority Filing Date ..... August 16, 2001  
Inventor ..... Charles H. Dennison et al.  
Assignee ..... Micron Technology, Inc.  
Priority Group Art Unit ..... 2825  
Priority Examiner ..... R. Rocchegiani  
Attorney's Docket No. .... MI22-2346  
TITLE: Semiconductor Processing Methods, and Semiconductor Assemblies

---

Commissioner for Patents  
Alexandria, VA 22313-1450

Attention: Official Draftsman

**SUBSTITUTE DRAWING REQUEST**

Please enter the enclosed substitute drawings in the above-referenced application in place of drawings originally filed. The content of the drawings are identical to those now on file in this application.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Date: 7-29-03

By:

  
D. Brent Kenady  
Reg. No.: 40,045  
WELLS ST. JOHN P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828  
(509) 624-4276

Enclosures: 6 Sheets of Formal Drawings, Figs. 1-11.